

Customer No.: 31561  
Docket No.: 12092-US-PA  
Application No.: 10/709,037

## **REMARKS**

### **I. Present Status of the Application**

Claims 11-14 are rejected under 35 U.S.C. 112 as being indefinite. Claims 1, 2 and 6-9 are rejected under 35 U.S.C. 102(b) as being anticipated by 5,844,645 to Kashimoto (Kashimoto). Claims 3-5 and 10-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kashimoto in view of US 2001/0019388 to Kim et al (Kim).

In response thereto, Applicants have canceled claims 1-10, amended claim 11, and added claims 15-24. The amendments and added claims can find support from the specification and drawings, and therefore no new matter is raised. After entry of the foregoing amendments, claims 11-24 remain pending in the present application, and reconsideration of those claims is respectfully requested.

### **II. Discussion of Rejection Under 35 U.S.C. 112**

The Office Action rejected claims 11-14 under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 11 sets forth the step of "simultaneously forming a first light shielding layer over the black matrix", however it is not clear which other step(s) is/are intended to be performed simultaneously with the formation of the first light shielding layer.

Customer No.: 31561  
Docket No.: 12092-US-PA  
Application No.: 10/709,037

In response thereto, Applicants have amended claim 11 to clearly define *simultaneously forming a first light shielding layer over the black matrix during the formation of the alignment bumps*. After the above amendment, claim 11 is believed to be definite and the rejection is respectfully requested to be withdrawn.

### III. Discussion of Rejection Under 35 U.S.C. 102

The Office Action rejected claims 1, 2 and 6-9 under 35 U.S.C. 102(b) as being anticipated by 5,844,645 to Kashimoto (Kashimoto).

In response thereto, Applicants have canceled claims 1, 2 and 6-9.

### IV. Discussion of Rejection Under 35 U.S.C. 103(a)

The Office Action rejected claims 3-5 and 10-14 under 35 U.S.C. 103(a) as being unpatentable over Kashimoto in view of Kim.

In response thereto, Applicants have canceled claims 3-5 and 10.

Regarding claim 11, Applicant has amended it to expressly define *simultaneously forming a first light shielding layer over the black matrix during the formation of the alignment bumps*.

In the Office Action, Examiner contended that Kashimoto does not disclose providing a plurality of alignment bumps but Kim discloses a multi-domain liquid crystal display device

Customer No.: 31561  
Docket No.: 12092-US-PA  
Application No.: 10/709,037

comprising dielectric structure (i.e. corresponding to the alignment bumps of the present invention) provided on a color filter substrate.

However, in Kim, as shown in FIGS. 2A-5, the dielectric structure 53 is disposed over the common electrode 17 which is formed on the light-shielding layer 25. *Kim fails to disclose simultaneously forming a first light shielding layer over the black matrix during the formation of the alignment bumps.*

Accordingly, neither Kashimoto nor Kim discloses *simultaneously forming a first light shielding layer over the black matrix during the formation of the alignment bumps*. Claim 11 should be patentable over Kashimoto in view of Kim. Reconsideration and withdrawal of the rejection and allowance of claim 11 is respectively requested.

Claims 12-14 should also be patentable since they depend on allowable claim 11 directly or indirectly.

#### IV. Discussion of newly added claims

With respect to newly added independent claim 15, it expressly defines a liquid crystal display panel which comprises a glue disposed between the thin film transistor array substrate and the black matrix to form a sealed space therebetween. In Kashimoto, the sealing material 40 is disposed between the TFT-array substrate 10 and the color-filter substrate 20, but the sealing material 40 is not disposed between the TFT-array substrate 10 and the light shielding layer 26. (FIGS. 4-5 of Kashimoto) As such, Kashimoto fails to disclose a glue

Customer No.: 31561  
Docket No.: 12092-US-PA  
Application No.: 10/709,037

**disposed between the thin film transistor array substrate and the black matrix to form a sealed space therebetween. Kim does also not disclose this feature.**

Therefore, newly added claim 15 should be patentable.

Newly added dependent claims 16-20 should also be patentable since each of them depends on allowable claim 15 directly.

With respect to newly added independent claim 21, it expressly define a process of forming a liquid crystal display panel which comprises **disposing a glue between the thin film transistor array substrate and the black matrix to form a sealed space therebetween**, which is not disclosed by Kashimoto or Kim.

Therefore, newly added independent claim 21 should be patentable.

Newly added dependent claims 22-24 should also be patentable since each of them depends on allowable claim 21 directly.

Customer No.: 31561  
Docket No.: 12092-US-PA  
Application No.: 10/709,037

**CONCLUSION**

For at least the foregoing reasons, it is believed that the pending claims 11-24 are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date :

June 1, 2007

Respectfully submitted,

Belinda Lee

Belinda Lee

Registration No.: 46,863

Jiang Chyun Intellectual Property Office  
7th Floor-1, No. 100  
Roosevelt Road, Section 2  
Taipei, 100  
Taiwan  
Tel: 011-886-2-2369-2800  
Fax: 011-886-2-2369-7233  
Email: belinda@jciipgroup.com.tw  
Usa@jciipgroup.com.tw